

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	18	((check\$3 or verif\$7 or validat\$3) with file\$1 with (chip near2 (design or outline or scheme or model or layout or sketch))))	USPAT	OR	OFF	2007/06/12 17:58
L2	1	((check\$3 or verif\$7 or validat\$3) with file\$1 with (chip near2 (design or outline or scheme or model or layout or sketch)))) same (compar\$5 with (file\$1 or (multiple\$1 near2 file\$1)))	USPAT	OR	OFF	2007/06/12 17:59
L3	1	((check\$3 or verif\$7 or validat\$3) with file\$1 with (chip near2 (design\$1 or outline\$1 or scheme\$1 or model\$1 or layout\$1 or sketch\$2)))) same (compar\$5 with (file\$1 or (multiple\$1 near2 file\$1)))	USPAT	OR	OFF	2007/06/12 18:02
L4	8	((check\$3 or verif\$7 or validat\$3) with file\$1 with (chip or (integrat\$2 near2 circuit) near2 (design\$1 or outline\$1 or scheme\$1 or model\$1 or layout\$1 or sketch\$2)))) same (compar\$5 with (file\$1 or (multiple\$1 near2 file\$1)))	USPAT	OR	OFF	2007/06/12 18:14
L5	2	((check\$3 or verif\$7 or validat\$3) with (data or (data near2 file\$1)) with (chip or (integrat\$2 near2 circuit) near2 (design\$1 or outline\$1 or scheme\$1 or model\$1 or layout\$1 or sketch\$2)))) same (compar\$5 with (file\$1 or (multiple\$1 near2 file\$1)))	USPAT	OR	OFF	2007/06/12 18:06
L6	2	((check\$3 or verif\$7 or validat\$3) with (data or (data near2 file\$1)) with (chip or (integrat\$2 near3 circuit) near2 (design\$1 or outline\$1 or scheme\$1 or model\$1 or layout\$1 or sketch\$2)))) same (compar\$5 with (file\$1 or (multiple\$1 near2 file\$1)))	USPAT	OR	OFF	2007/06/12 18:06
L7	14	((check\$3 or verif\$7 or validat\$3) with (chip or (integrat\$2 near2 circuit) near2 (design\$1 or outline\$1 or scheme\$1 or model\$1 or layout\$1 or sketch\$2)))) same (compar\$5 with (file\$1 or (multiple\$1 near2 file\$1)))	USPAT	OR	OFF	2007/06/12 18:20
L8	20352	"707"/\$.ccls.	USPAT	OR	OFF	2007/06/12 18:15
L9	0	7 and 8	USPAT	OR	OFF	2007/06/12 18:15
L10	3	7 and (@ad<"20000131" or @rlad<"2000131")	USPAT	OR	OFF	2007/06/12 18:19

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L11	3	7 and (@ad<"20000131" or @rlad<"2000131")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/12 18:20
L12	44	((check\$3 or verif\$7 or validat\$3) with (chip or (integrat\$2 near2 circuit) near2 (design\$1 or outline\$1 or scheme\$1 or model\$1 or layout\$1 or sketch\$2))) same (compar\$5 with (file\$1 or (multiple\$1 near2 file\$1)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/12 18:21
L13	5	12 and (@ad<"20000131" or @rlad<"2000131")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/12 18:32
L14	316	((check\$3 or verif\$7 or validat\$3) with (chip\$1 or microprocessor\$1 or processor\$1 or (integrat\$2 near2 circuit) near2 (design\$1 or outline\$1 or scheme\$1 or model\$1 or layout\$1 or sketch\$2))) same (compar\$5 with (file\$1 or (multiple\$1 near2 file\$1)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/12 18:23
L15	147	14 and (@ad<"20000131" or @rlad<"2000131")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/12 18:22
L16	316	((check\$3 or verif\$7 or validat\$3) with (single-chip\$1 or chip\$1 or ("single-chip processing unit") or microprocessor\$1 or processor\$1 or (integrat\$2 near2 circuit) near2 (design\$1 or outline\$1 or scheme\$1 or model\$1 or layout\$1 or sketch\$2))) same (compar\$5 with (file\$1 or (multiple\$1 near2 file\$1)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/12 18:32

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L17	387	((check\$3 or verif\$7 or validat\$3) with (single-chip\$1 or chip\$1 or ("single-chip processing unit") or microprocessor\$1 or processor\$1 or (integrat\$2 near2 circuit) near2 (design\$1 or outline\$1 or scheme\$1 or model\$1 or layout\$1 or sketch\$2))) same (compar\$5 with (file\$1 or profile\$1 or (multiple\$1 near2 file\$1)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/12 18:32
L18	178	17 and (@ad<"20000131" or @rlad<"2000131")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/12 18:34
L19	419	((check\$3 or verif\$7 or validat\$3) with (compar\$5 with (file\$1 or profile\$1 or (multiple\$1 near2 file\$1))) same (single-chip\$1 or chip\$1 or ("single-chip processing unit") or microprocessor\$1 or processor\$1 or (integrat\$2 near2 circuit) near2 (design\$1 or outline\$1 or scheme\$1 or model\$1 or layout\$1 or sketch\$2)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/12 18:33
L20	230	((check\$3 or verif\$7 or validat\$3) with (compar\$5 with (file\$1 or profile\$1 or (multiple\$1 near2 file\$1))) with (single-chip\$1 or chip\$1 or ("single-chip processing unit") or microprocessor\$1 or processor\$1 or (integrat\$2 near2 circuit) near2 (design\$1 or outline\$1 or scheme\$1 or model\$1 or layout\$1 or sketch\$2)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/12 18:34
L21	104	20 and (@ad<"20000131" or @rlad<"2000131")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/12 18:34

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	18265	(file\$1 near2 (validat\$4 or check\$3 or verif\$5))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:25
L2	112	(file\$1 near2 (validat\$4 or check\$3 or verif\$5)) same ((intergrated near2 circuit) or chip\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 15:52
L3	0	(file\$1 near2 (validat\$4 or check\$3 or verif\$5)) same ((intergrated near2 circuit) or chip\$1) same (compar\$5 with (timestamp or temporal))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 15:54
L4	31	(file\$1 near2 (validat\$4 or check\$3 or verif\$5)) same (compar\$5 with (timestamp or temporal))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:29
L5	8	4 and ((intergrated near2 circuit) or chip\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:19
L6	1	5 and (@ad<"20000124" or @rlad<"20000124")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:25
L7	117385	((file\$1 or folder\$1 or data) near2 (validat\$4 or check\$3 or verif\$5))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:16

EAST Search History

L8	117385	((file\$1 or folder\$1 or data) near2 (validat\$4 or check\$3 or checksum or verif\$5))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:16
L9	25132	8 and ((intergrated near2 circuit) or chip\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:17
L10	12249	9 and (@ad<"20000124" or @rlad<"20000124")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:17
L11	8	4 and ((intergrated near2 circuit) or chip\$1 or ((small-scale or medium-scale or medium-scale or large-scale or very-large-scale) near2 intergration))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:30
L12	11	4 and ((intergrated near2 circuit) or chip\$1 or CPU or "central processing unit" or ((small-scale or medium-scale or medium-scale or large-scale or very-large-scale) near2 intergration))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 18:25
L13	1	12 and (@ad<"20000124" or @rlad<"20000124")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:26
L14	607	1 and 707/200,203.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:31

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L15	254	14 and (@ad<"20000124" or @rlad<"20000124")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:30
L16	6727	1 and ((intergrated near2 circuit) or chip\$1 or CPU or "central processing unit" or ((small-scale or medium-scale or medium-scale or large-scale or very-large-scale) near2 intergration))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:40
L17	273	(file\$1 near2 (validat\$4 or check\$3 or verif\$5)) same (compar\$5 with (timestamp or temporal or date or year ot time))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:38
L18	36	17 and ((intergrated near2 circuit) or chip\$1 or ((small-scale or medium-scale or medium-scale or large-scale or very-large-scale) near2 intergration))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:30
L19	12	18 and (@ad<"20000124" or @rlad<"20000124")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:40
L20	0	19 and 707/200,203.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:41
L21	2	19 and "707"/\$.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:31

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L22	0	(file\$1 near2 (validat\$4 or check\$3 or verif\$5)) same (compar\$5 with (timestamp or temporal or date or year ot time)) same ((intergrated near2 circuit) or chip\$1 or ((small-scale or medium-scale or medium-scale or large-scale or very-largec-scale) near2 intergration))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:38
L23	278	(file\$1 near2 (validat\$4 or check\$3 or verif\$5)) same (compar\$5 with (timestamp or temporal or date\$1 or year\$1 or time or ((built or build or creation\$1) near2 time)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:40
L24	403	(file\$1 near2 (validat\$4 or check\$3 or verif\$5)) same (compar\$5 with (timestamp or temporal or version\$1 or date\$1 or year\$1 or time or ((built or build or creation\$1) near2 time)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:00
L25	158	24 and ((intergrated near2 circuit) or chip\$1 or CPU or "central processing unit" or ((small-scale or medium-scale or medium-scale or large-scale or very-largec-scale) near2 intergration))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:04
L26	63	25 and (@ad<"20000124" or @rlad<"20000124")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:05
L27	8	26 and 707/200,203.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:42
L28	20	26 and "707"/\$.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:02

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L29	1	26 and "703"/23.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:41
L30	1	26 and "700"/169.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:41
L31	1	26 and "726"/16.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:41
L32	6	26 and "709"/203,223,213.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:42
L33	4	26 and "713"/176,100.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 16:42
L34	403	(file\$1 near2 (validat\$4 or check\$3 or verif\$5)) same (compar\$5 with (timestamp or temporal or version\$1 or date\$1 or year\$1 or time or ((built or modif\$5 or updat\$3 or build or creation\$1) near2 time)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:06
L35	158	25 and ((intergrated near2 circuit) or chip\$1 or CPU or "central processing unit" or ((small-scale or medium-scale or medium-scale or large-scale or very-large-scale) near2 intergration))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:14

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L36	63	35 and (@ad<"20000124" or @rlad<"20000124")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:01
L37	158	24 and (((intergrated near2 circuit) or chip\$1 or CPU or "central processing unit" or ((small-scale or medium-scale or processor\$1 or microprocessor\$1 or medium-scale or large-scale or very-largec-scale) near2 intergration)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:04
L38	63	37 and (@ad<"20000124" or @rlad<"20000124")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:11
L39	6	(file\$1 near2 (validat\$4 or check\$3 or verif\$5)) same (compar\$5 with (timestamp or temporal or version\$1 or date\$1 or year\$1 or time or ((built or modif\$5 or updat\$3 or build or creation\$1) near2 time)) with (equivalent or equal\$5 or "="))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:14
L40	6	(file\$1 near2 (validat\$4 or check\$3 or verif\$5 or certif\$5, endorse or ratif\$7 or legaliz\$7)) same (compar\$5 with (timestamp or temporal or version\$1 or date\$1 or year\$1 or time or ((built or modif\$5 or updat\$3 or build or creation\$1) near2 time)) with (equivalent or equal\$5 or "="))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:12
L41	1	40 and (@ad<"20000124" or @rlad<"20000124")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:28
L43	7	(file\$1 near2 (validat\$4 or check\$3 or verif\$7 or certif\$5 or ratif\$7 or legaliz\$7)) same (compar\$5 with (timestamp or temporal or version\$1 or date\$1 or year\$1 or time or ((built or modif\$5 or updat\$3 or build or creation\$1) near2 time)) with (equivalent or equal\$5 or "="))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:12

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L44	0	43 and ((intergrated near2 circuit) or chip\$1 or CPU or "central processing unit" or ((small-scale or medium-scale or medium-scale or large-scale or very-largec-scale) near2 intergration))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:14
L45	62	(file\$1 near2 (validat\$4 or check\$3 or verif\$5)) and (compar\$5 with (timestamp or temporal or version\$1 or date\$1 or year\$1 or time or ((built or modif\$5 or updat\$3 or build or creation\$1) near2 time)) with (equivalent or equal\$5 or "=")) and ((intergrated near2 circuit) or chip\$1 or CPU or "central processing unit" or ((small-scale or medium-scale or medium-scale or large-scale or very-largec-scale) near2 intergration))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:15
L46	1276504	"62" and (@ad<"20000124" or @rlad<"20000124")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 17:33
L47	27	45 and (@ad<"20000124" or @rlad<"20000124")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/21 18:17